

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

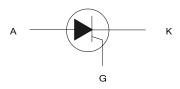
# US112S/N

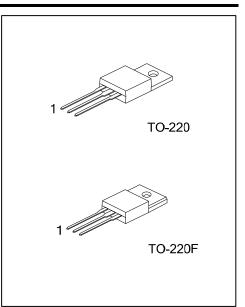
## **SCRS**

#### DESCRIPTION

The UTC US112S/N is suitable to fit all modes of control found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, in-rush current limiting circuits, capacitive discharge ignition, voltage regulation circuits.

#### SYMBOL



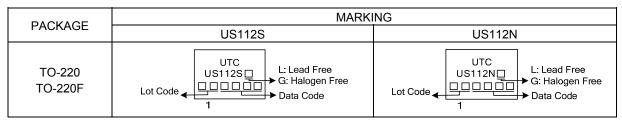


#### **ORDERING INFORMATION**

Ordering Number		Deelvere	Pin	Dealises		
Lead Free	Halogen Free	Package	1	2	3	Packing
US112SL-x-TA3-T	US112SG-x-TA3-T	TO-220	К	А	G	Tube
US112SL-x-TF3-T	US112SG-x-TF3-T	TO-220F	К	А	G	Tube
US112NL-x-TA3-T	US112NG-x-TA3-T	TO-220	К	А	G	Tube
US112NL-x-TF3-T	US112NG-x-TF3-T	TO-220F	К	Α	G	Tube
Note: Pin Assignment: K: Cathode A: Anode G: Gate						

US112SL-x-TA3-T		
(1)Packing Type	(1) T: Tube	
(2)Package Type	(2) TA3: TO-220, TF3: TO-220F	
(3)Peak Voltage	(3) 4: 400V, 6: 600V, 8: 800V	
(4)Lead Free	(4) L: Lead Free, G: Halogen Free	

#### MARKING INFORMATION



#### ■ ABSOLUTE MAXIMUM RATING

PARAMETER			RATING	UNIT
	US112S/N-4		400	
Repetitive Peak Off-State Voltages	US112S/N-6	V <sub>DRM</sub> V <sub>RRM</sub>	600	V
	US112S/N-8		800	
RMS On-State Current (180°Conduction Angle) (	I <sub>T(RMS)</sub>	12	Α	
Average On-State Current (180°Conduction Angle) (T <sub>c</sub> = 110°C)			8	Α
Non Repetitive Surge Peak On-State Current	t <sub>P</sub> =8.3ms		146	
(T <sub>J</sub> = 25°C)	t <sub>P</sub> =10ms	ITSM	140	A
l <sup>2</sup> t Value For Fusing (t <sub>P</sub> = 10 ms ,T <sub>J</sub> = 25 $^{\circ}$ C)	l²t	98	A²S	
Critical Rate Of Rise Of On-State Current	dl/dt	50	A /	
(I <sub>G</sub> = 2 x I <sub>GT</sub> , t <sub>R</sub> ≤ 100 ns, T <sub>J</sub> = 125°C)		50	A/µs	
Peak Gate Current (t <sub>P</sub> =20µs, F = 60 Hz, T <sub>J</sub> =125°	I <sub>GM</sub>	4	Α	
Peak Reverse Gate Voltage US112N		V <sub>RGM</sub>	5	V
Average Gate Power Dissipation (T <sub>J</sub> = 125°C)	P <sub>G(AV)</sub>	1	W	
Storage Temperature		T <sub>STG</sub>	-40 ~ +150	°C
Junction Temperature	ΤJ	+125	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Case		θ <sub>JA</sub>	60	K/W
Junction to Ambient	TO-220	0	1.3	
	TO-220F	θις	2.3	K/W

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C unless otherwise specified)

#### US112S(SENSITIVE)

			NAINI		MANY	
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	INAX	UNIT
Gate Trigger Current	I <sub>GT</sub>	$V_{\rm D}$ = 12V, R <sub>L</sub> =140 $\Omega$			200	μA
Gate Trigger Voltage	$V_{GT}$	V <sub>D</sub> = 12V, R <sub>L</sub> =140Ω			0.8	V
Gate Non-Trigger Voltage	$V_{\text{GD}}$	V <sub>D</sub> = V <sub>DRM</sub> , R <sub>L</sub> = 3.3kΩ, R <sub>GK</sub> = 1KΩ, T <sub>J</sub> = 125℃	0.1			V
Reverse Gate Voltage	$V_{RG}$	I <sub>RG</sub> = 10 μΑ	8			V
Holding Current	I <sub>H</sub>	I <sub>T</sub> = 50mA, R <sub>GK</sub> = 1kΩ			5	mA
Latching Current	١ <sub>L</sub>	$I_G = 1 \text{mA}$ , $R_{GK} = 1 \text{k}\Omega$			6	mA
Circuit Rate of Change of Off-State Voltage	dV/dt	$V_{D} = 67\% V_{DRM}, R_{GK} = 220\Omega$	5			V/µs
On-State Voltage	V <sub>TM</sub>	I <sub>TM</sub> =24A, t <sub>P</sub> = 380 μs			1.6	V
Threshold Voltage	V <sub>T0</sub>	T」= 125℃			0.85	V
Dynamic Resistance	R <sub>D</sub>	T」= 125℃			30	mΩ
Off State Leekane Current	I <sub>DRM</sub>	$V_{DRM} = V_{RRM}, R_{GK} = 220\Omega$			5	μA
Off-State Leakage Current	I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM,</sub> R <sub>GK</sub> =220Ω, T <sub>J</sub> = 125°C			2	mA



## ■ ELECTRICAL CHARACTERISTICS(Cont.)

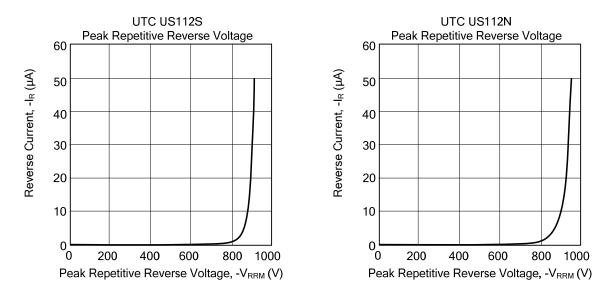
#### US112N(SENSITIVE)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I <sub>GT</sub>	V <sub>D</sub> = 12 V, R <sub>L</sub> =33Ω	2		15	mA
Gate Trigger Voltage	$V_{GT}$	V <sub>D</sub> = 12 V, R <sub>L</sub> =33Ω			1.3	V
Gate Non-Trigger Voltage	$V_{GD}$	V <sub>D</sub> =V <sub>DRM</sub> , R <sub>L</sub> = 3.3kΩ, T <sub>J</sub> =125°C	0.2			V
Holding Current	I <sub>H</sub>	I⊤ = 500mA Gate open			30	mA
Latching Current	ΙL	I <sub>G</sub> = 1.2 I <sub>GT</sub>			60	mA
Circuit Rate of Change of Off-State Voltage	dV/dt	V <sub>D</sub> =67% V <sub>DRM</sub> Gate open, Tյ=125°C	200			V/µs
On-State Voltage	V <sub>TM</sub>	I <sub>TM</sub> =24 A, t <sub>P</sub> = 380 μs			1.6	V
Threshold Voltage	V <sub>T0</sub>	T」= 125°C			0.85	V
Dynamic Resistance	R <sub>D</sub>	T <sub>J</sub> = 125°C			30	mΩ
Off Otata Laskana Oursent	I <sub>DRM</sub>	$V_{DRM} = V_{RRM}$			5	μA
Off-State Leakage Current	I <sub>RRM</sub>	$V_{DRM} = V_{RRM}, T_J = 125^{\circ}C$			2	mA



# US112S/N

### TYPICAL CHARACTERISTICS



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