



# X0202/A/B

**SCR**

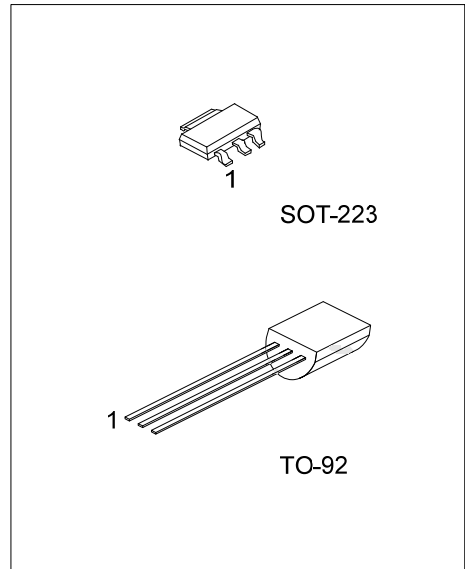
## SENSITIVE SCRS

### DESCRIPTION

The UTC **X0202/A/B** SCR series is suitable for all applications where the available gate current is limited, such as ground fault circuit interruptors, overvoltage crowbar protection in low power supplies, capacitive ignition circuit, .....

### FEATURES

- \*  $I_{T(RMS)}$  : 1.25A
- \*  $V_{DRM}/V_{RRM}$  : 600/800/1000V



### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
-	X0202G-AA3-R	SOT-223	K	A	G	Tape Reel
X0202L-T92-B	X0202G-T92-B	TO-92	K	G	A	Tape Box
X0202L-T92-K	X0202G-T92-K	TO-92	K	G	A	Bulk
X0202L-T92-R	X0202G-T92-R	TO-92	K	G	A	Tape Reel
-	X0202xG-AA3-R	SOT-223	K	A	G	Tape Reel
X0202xL-T92-B	X0202xG-T92-B	TO-92	K	G	A	Tape Box
X0202xL-T92-K	X0202xG-T92-K	TO-92	K	G	A	Bulk
X0202xL-T92-R	X0202xG-T92-R	TO-92	K	G	A	Tape Reel

Note: Pin Assignment: K: Cathode G: Gate A: Anode

<p>X0202xG-AA3-R</p> <ul style="list-style-type: none"> <li>(1)Packing Type</li> <li>(2)Package Type</li> <li>(3)Green Package</li> <li>(4)Peak Voltage</li> </ul>	<ul style="list-style-type: none"> <li>(1) B: Tape Box, K: Bulk, R: Tape Reel</li> <li>(2) AA3: SOT-223, T92: TO-92</li> <li>(3) G: Halogen Free and Lead Free, L: Lead Free</li> <li>(4) Blank: 600V, A: 800V, B: 1000V</li> </ul>
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### MARKING

Package	MARKING	
	X0202	X0202x
SOT-223		
TO-92		

■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)

PARAMETERS		SYMBOL	RATINGS	UNIT
Peak Repetitive Forward and Reverse Blocking Voltage ( $T_J=110^{\circ}\text{C}$ , $R_{GK}=1\text{k}\Omega$ )	X0202	$V_{\text{DRM}}$ , $V_{\text{RRM}}$	600	V
	X0202A		800	V
	X0202B		1000	V
RMS On-State Current 180°C Conduction Angle	$T_{\text{tab}}=95^{\circ}\text{C}$	$I_{\text{T(RMS)}}$	1.25	A
Average On-State Current 180°C Conduction Angle	$T_{\text{tab}}=95^{\circ}\text{C}$	$I_{\text{T(AV)}}$	0.8	A
Non Repetitive Surge Peak on-State Current ( $t_p=8.3\text{ms}$ $T_J=25^{\circ}\text{C}$ )		$I_{\text{TSM}}$	25	A
Non Repetitive Surge Peak on-State Current ( $t_p=10\text{ms}$ $T_J=25^{\circ}\text{C}$ )		$I_{\text{TSM}}$	22.5	A
$I^2t$ Value for Fusing ( $t_p=10\text{ms}$ $T_J=25^{\circ}\text{C}$ )		$I^2t$	2.5	$\text{A}^2\text{S}$
Critical Rate Of Rise Of On-state Current $I_G=2 \times I_{GT}$ , $t_r \leq 100\text{ns}$ , $f=60\text{Hz}$ , $T_J=125^{\circ}\text{C}$		$di/dt$	50	$\text{A}/\mu\text{s}$
Peak Gate Current ( $p=20\mu\text{s}$ $T_J=125^{\circ}\text{C}$ )		$I_{GM}$	1.2	A
Average Gate Power Dissipation ( $T_J=125^{\circ}\text{C}$ )		$P_{G(AV)}$	0.2	W
Operating Junction Temperature Range		$T_J$	-40 ~ +125	$^{\circ}\text{C}$
Storage Junction Temperature Range		$T_{\text{STG}}$	-40 ~ +150	$^{\circ}\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

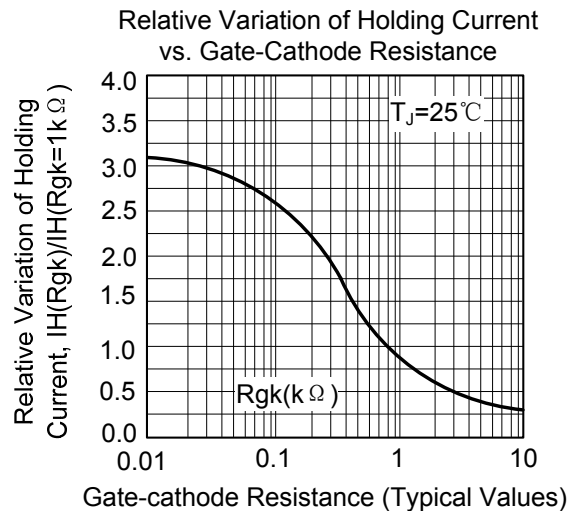
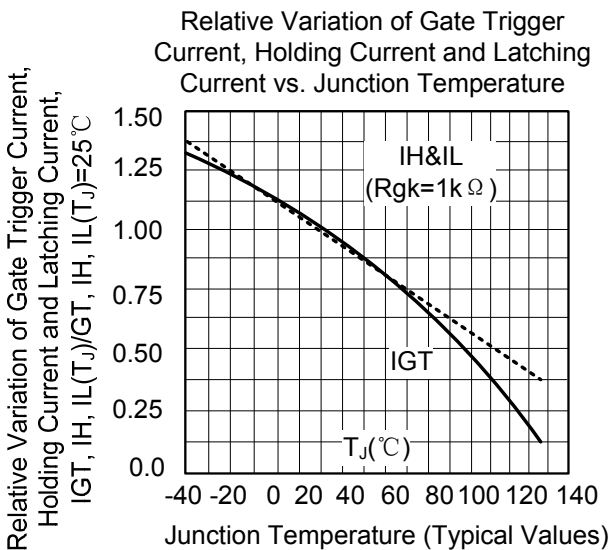
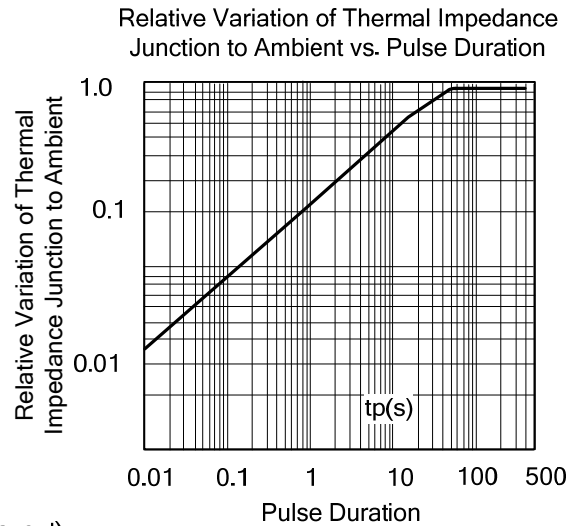
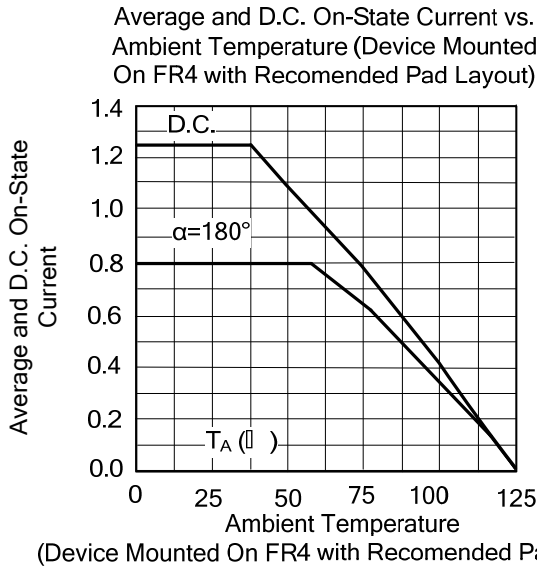
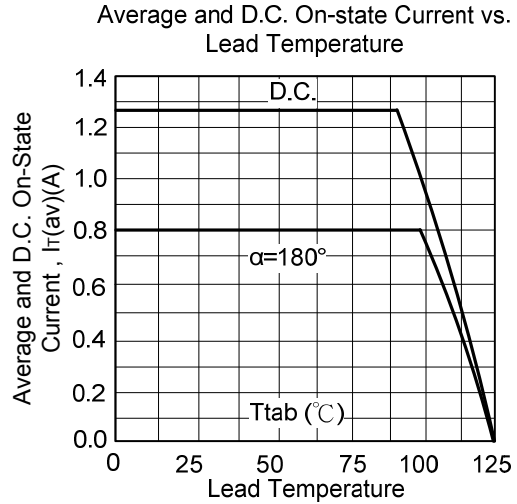
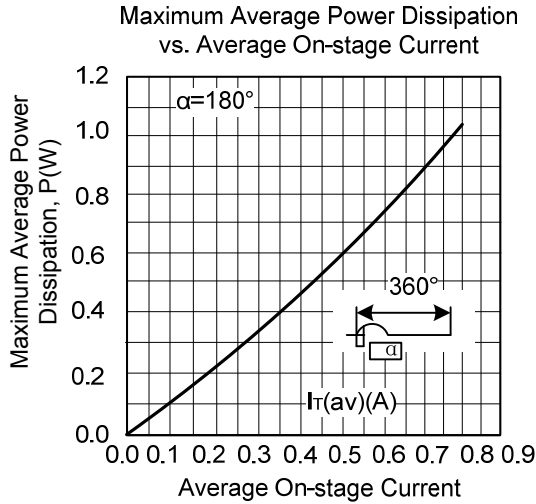
PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Tab	SOT-223	$\theta_{JT}$	25	$^{\circ}\text{C}/\text{W}$
	TO-92		60	$^{\circ}\text{C}/\text{W}$
Junction to Ambient (S=5cm)	SOT-223	$\theta_{JA}$	60	$^{\circ}\text{C}/\text{W}$
	TO-92		150	$^{\circ}\text{C}/\text{W}$

S=Copper surface under tab

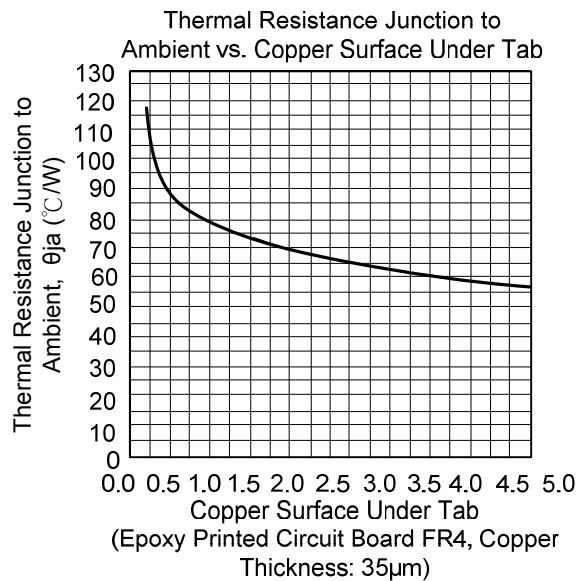
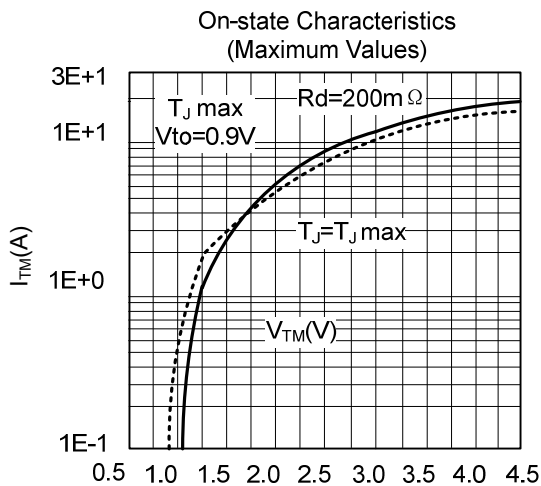
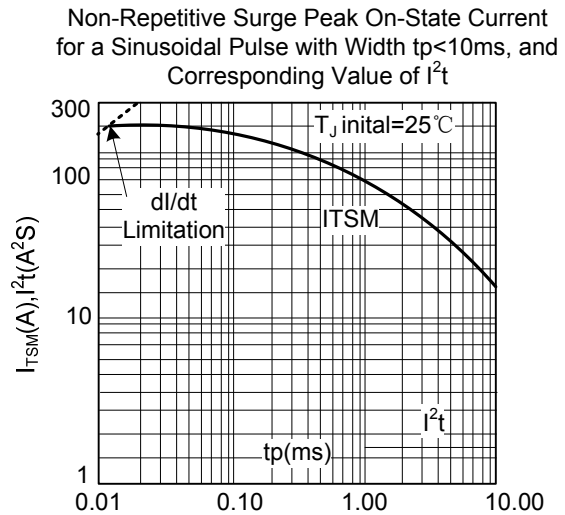
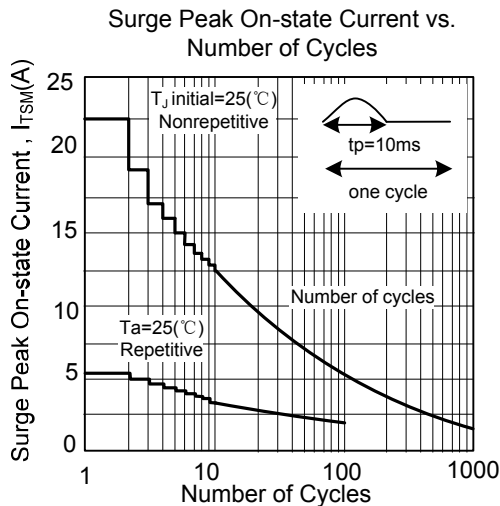
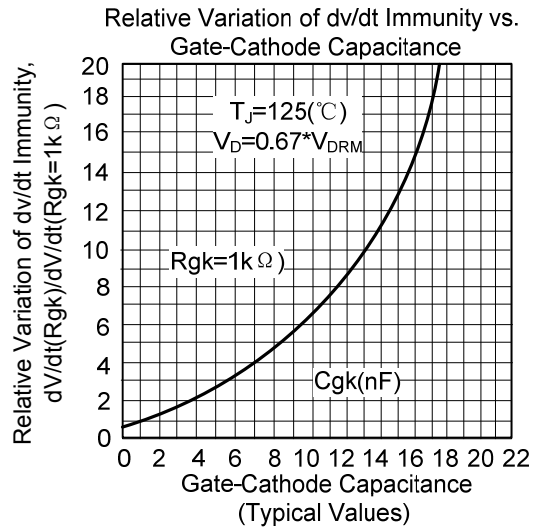
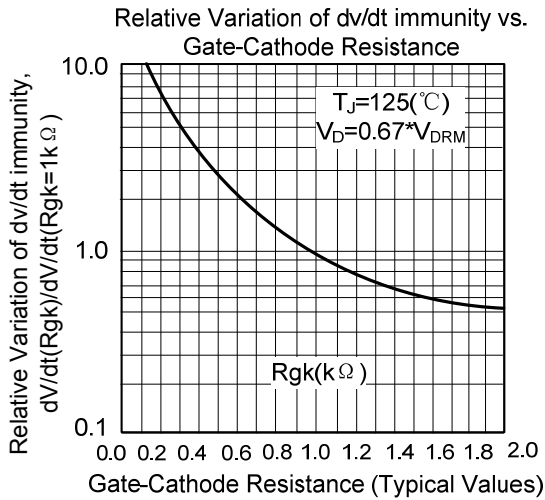
■ ELECTRICAL CHARACTERISTI

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Peak Forward or Reverse Blocking Current	$T_J=25^{\circ}\text{C}$	$I_{\text{DRM}}$ , $I_{\text{RRM}}$	$V_{\text{DRM}}=V_{\text{RRM}}$ , $R_{GK}=1\text{k}\Omega$			5	$\mu\text{A}$
	$T_J=125^{\circ}\text{C}$					500	$\mu\text{A}$
Peak Forward On-State Voltage		$V_{\text{TM}}$	$I_{\text{TM}}=2.5\text{A}$ , $t_p=380\mu\text{s}$			1.45	V
Gate Trigger Current		$I_{\text{GT}}$	$V_D=12\text{V}$ , $R_L=140\Omega$			200	$\mu\text{A}$
Gate Trigger Voltage		$V_{\text{GT}}$	$V_D=12\text{V}$ , $R_L=140\Omega$			0.8	V
Gate Non-Trigger Voltage		$V_{\text{GD}}$	$V_D=V_{\text{DRM}}$ , $R_L=3.3\text{k}\Omega$ , $R_{GK}=1\text{k}\Omega$ , ( $T_J=125^{\circ}\text{C}$ )	0.1			V
Holding Current		$I_{\text{H}}$	$I_T=50\text{mA}$ , $R_{GK}=1\text{k}\Omega$			5	mA
Latch Current		$I_{\text{L}}$	$I_G=1\text{mA}$ , $R_{GK}=1\text{k}\Omega$			6	mA
Critical Rate of Rise of Off-State Voltage		$dv/dt$	$V_D=67\%V_{\text{DRM}}$ , $R_{GK}=1\text{k}\Omega$ , ( $T_J=110^{\circ}\text{C}$ )	10			$\text{V}/\mu\text{s}$
Peak Reversed Gate Voltage		$V_{\text{RG}}$	$I_{\text{RG}}=10\mu\text{A}$	8			V
Threshold Voltage		$V_{\text{TO}}$	( $T_J=125^{\circ}\text{C}$ )			0.9	V
Dynamic Resistance		$R_d$	( $T_J=125^{\circ}\text{C}$ )			200	m $\Omega$

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



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