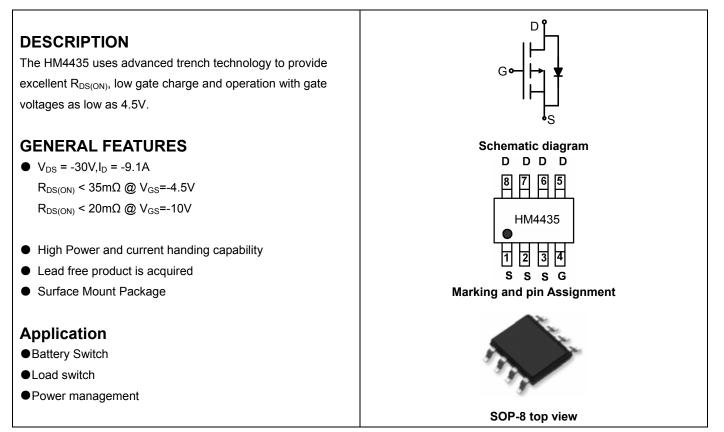
P-Channel Enhancement Mode Power MOSFET



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4435	HM4435	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	-9.1	A
Drain Current-Pulsed (Note 1)	I _{DM}	-50	A
Maximum Power Dissipation	PD	3.1	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	40	°C/W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V I _D =-250µA	-30	-33	-	V

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HM4435

Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	· · ·					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1	-1.5	-3	V
Drain-Source On-State Resistance	Р	V _{GS} =-10V, I _D =-9.1A		15	20	mΩ
Diam-Source On-State Resistance	R _{DS(ON)}	V_{GS} =-4.5V, I _D =-6.9A	-	21	35	mΩ
Forward Transconductance	g fs	V _{DS} =-15V,I _D =-9.1A	10	-	-	S
Dynamic Characteristics (Note4)	· · ·					
Input Capacitance	C _{lss}	(1 - 45)(1)(1 - 0)(1)	-	1600	-	PF
Output Capacitance	C _{oss}	V _{DS} =-15V,V _{GS} =0V, F=1.0MHz	-	350	-	PF
Reverse Transfer Capacitance	C _{rss}			300	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	tr	V _{DD} =-15V, ID=-1A,	-	15	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	110	-	nS
Turn-Off Fall Time	t _f			70	-	nS
Total Gate Charge	Qg	(1 - 45)(1 - 0.4)	-	30	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-9.1A V _{GS} =-10V	-	5.5	-	nC
Gate-Drain Charge	Q _{gd}	v _{GS} =-10v	-	8	-	nC
Drain-Source Diode Characteristics			·	•		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-2.1A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

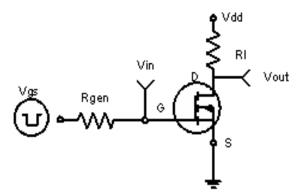
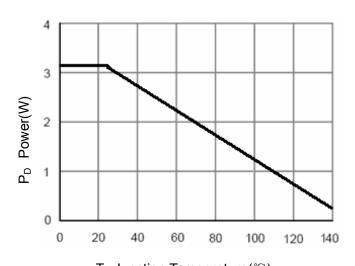
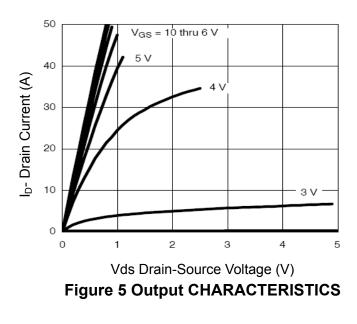
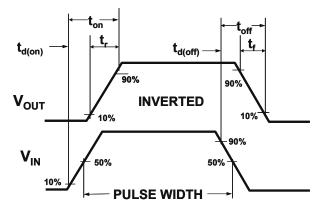


Figure 1:Switching Test Circuit

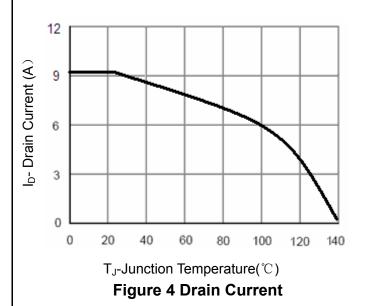


T_J-Junction Temperature(℃) **Figure 3 Power Dissipation**









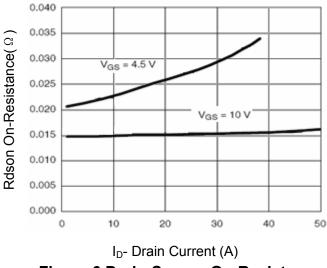
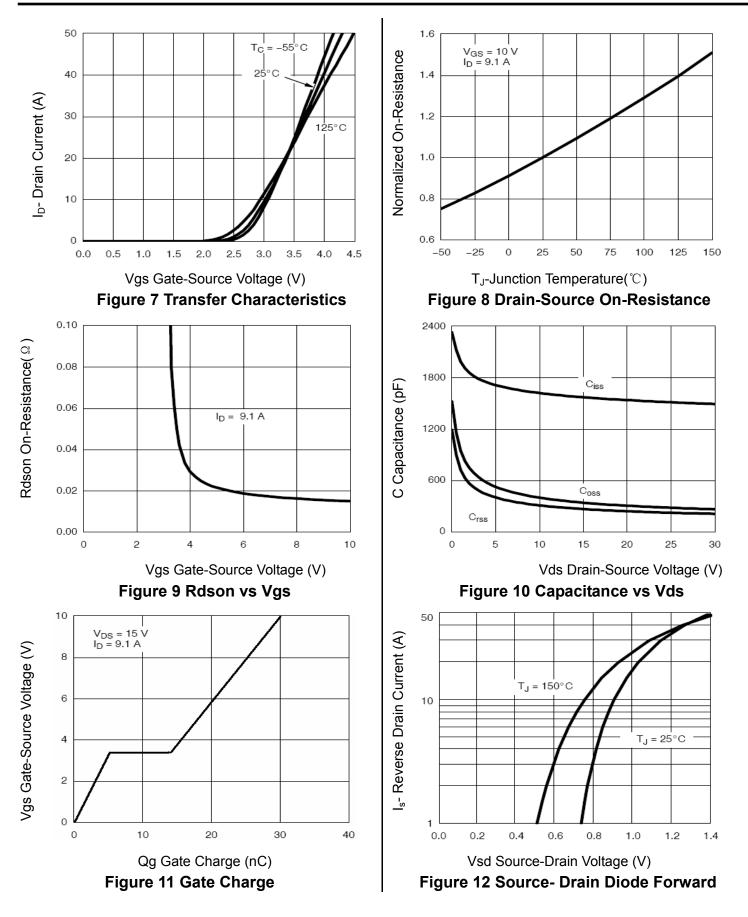


Figure 6 Drain-Source On-Resistance



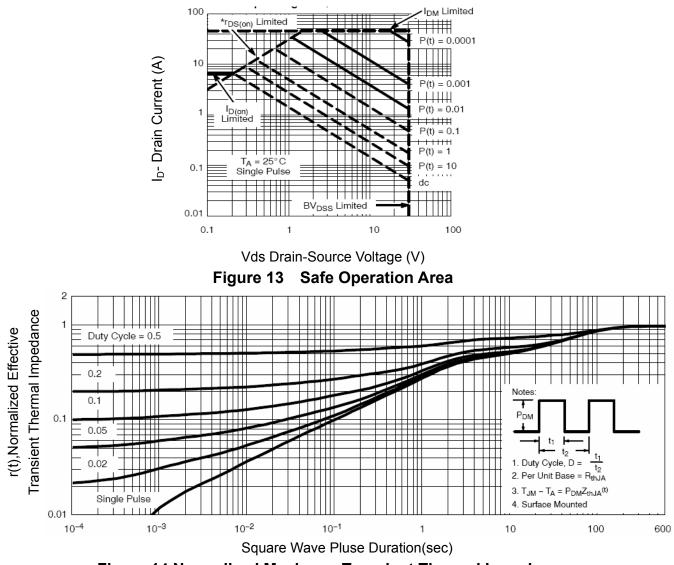
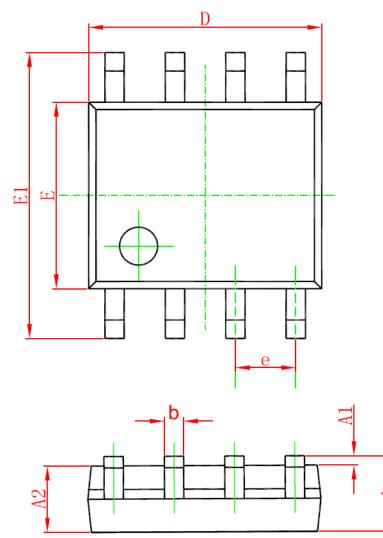
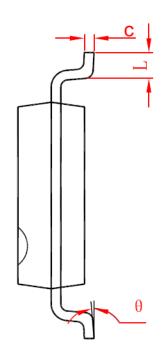


Figure 14 Normalized Maximum Transient Thermal Impedance

SOP-8 PACKAGE IN FORMATION





C. m.h. a l	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0.053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1.350	1. 550	0.053	0. 061	
b	0. 330	0. 510	0.013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
E	3.800	4.000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
e	1. 270	(BSC)	0. 050 (BSC)		
L	0. 400	1. 270	0.016	0. 050	
θ	0°	8°	0°	8°	

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