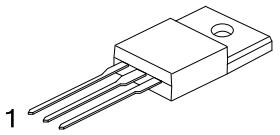
MGBR40L150C

Preliminary ***DIODE***

MOS GATED BARRIER RECTIFIER



TO-220F

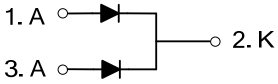
■ DESCRIPTION

The UTC **MGBR40L150C** is a surface mount mos gatedbarrier rectifier,it uses UTC's advanced technology to provide customers withlow forward voltage drop and high switching speed, etc.

■ FEATURES

* Low forward voltage drop
* High switching speed

■ SYMBOL



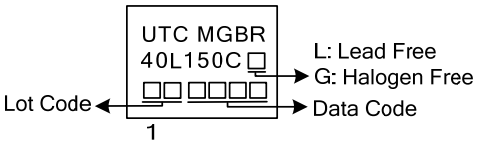
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
MGBR40L150CL-TF3-T	MGBR40L150CG-TF3-T	TO-220F	A	K	A	Tube

Note: Pin Assignment: A: Anode K: Common Cathode

MGBR40L150CL-TF3-T (1)Packing Type	(1) T: Tube
(2)Package Type	(2) TF3: TO-220F
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load,derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
Repetitive Peak Reverse Voltage		V_{RRM}	150	V
Average Rectified Output Current Per Device	Per Leg	I_O	20	A
	Total		40	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I_{FSM}	250	A
Operating Junction Temperature		T_J	-65~+150	°C
Storage Temperature		T_{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	3.31	°C/W

■ ELECTRICAL CHARACTERISTICS(T_A=25°C,unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I_R=0.5mA	150			V
Forward Voltage Drop	V_{FM}	I_F=20A, T_J=25°C			0.90	V
		I_F=20A, T_J=125°C			0.82	V
Leakage Current (Note 1)	I_{RM}	V_R=150V, T_J=25°C			100	μA
		V_R=150V, T_J=125°C			15	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.
2. Thermal resistance junction to case mounted on heatsink.

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