# UNISONIC TECHNOLOGIES CO., LTD

AZ23C **Preliminary DIODE** 

# **VOLTAGE REGULATOR DOUBLE DIODES**

#### **DESCRIPTION**

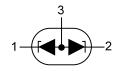
The UTC AZ23C is a voltage regulator double diode, it uses UTC's advanced technology to provide the customers with low leakage current.

The UTC AZ23C is suitable for ESD and surge protection.



\* Low leakage current

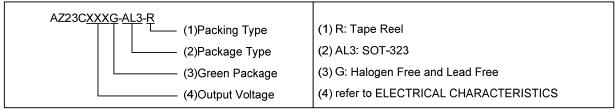
### **SYMBOL**





Ordering Number	Daakaga	Pin	Assignm	Dooking		
Ordering Number	Package	1	2	3	Packing	
AZ23CXXXG-AL3-R	SOT-323	K	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Cathode



#### MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING				
SOT-323	5.1: 5.1V	ZXXXG → Voltage Code				

SOT-323

# ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise specified)

PARAMETER	TST CONDITIONS	SYMBOL	RATINGS	UNIT
Continuous Forward Current		l <sub>F</sub>	200	mA
On-Repetitive Peak Reverse Current	t <sub>P</sub> =100us,square wave, T <sub>A</sub> =25°C, prior to surge	I <sub>ZSM</sub>	See TABLE 1	
Total Dayyar Dissination (Note 4)	T <sub>A</sub> =25°C, 2 diodes loaded		350	mW
Total Power Dissipation (Note 1)	T <sub>A</sub> =25°C, 1 diode loaded	P <sub>D</sub>	180	mW
Non-Repetitive Peak Reverse Dissipation	t <sub>P</sub> =100us, square wave, T <sub>A</sub> =25°C, prior to surge	P <sub>ZSM</sub>	40	W
Operating Junction Temperature		TJ	150	°C
Storage Temperature		T <sub>STG</sub>	-65~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# **■ THERMAL CHARACTERISTICS**

PARAMETER	SYMBOL	TST CONDITIONS	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	2 diodes loaded (Note 2)	355	K/W
		1 diodes loaded (Note 2)	680	K/W

Notes: 1. Solder points on cathode tabs.

2. Device mounted on a FR4 printed-circuit board.

# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, unless otherwise specified)

Device	Marking Code	Nominal Zener Voltage			Max Zener Impedance			Diode CAP. C <sub>d</sub> (pF)	Non-Rep etitive Peak Reverse Curren I <sub>ZSM</sub> (A)	Max Reverse Leakage Current (µA)		
		V	z <b>@ I</b> zт (	V)	ZZT (	@ IZT	ZZT (	@ IZT	f=1 MHz, V <sub>R</sub> =0V	t <sub>p</sub> =100µs T <sub>A</sub> =25°C	IR @	VR
		MIN	TYP	MAX	(Ω)	(mA)	(Ω)	(mA)	MAX	MAX	(uA)	(V)
AZ23C5V1	5.1	4.8	5.1	5.4	480	1	60	5	300	6.0	2	2

Note: Short duration pulse test used to minimize self-heating effect.

<sup>2.</sup> Device mounted on an FR4 printed-circuit board

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