

UNISONIC TECHNOLOGIES CO., LTD

SBL3045C Preliminary DIODE

LOW DROP POWER SCHOTTKY RECTIFIER

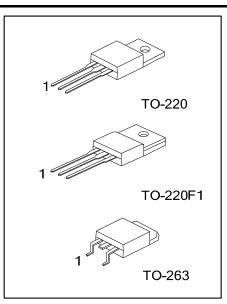
■ DESCRIPTION

The UTC **SBL3045C** is a dual center tap schottky rectifiers, it uses UTC's advanced technology to provide the customers with low forward voltage, high switching speed and low thermal resistance, etc.

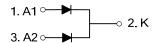
The UTC **SBL3045C** is suitable for high frequency DC-DC converter and switched mode power supplies, etc.

■ FEATURES

- * High switching speed
- * Low forward voltage drop
- * Low thermal resistance



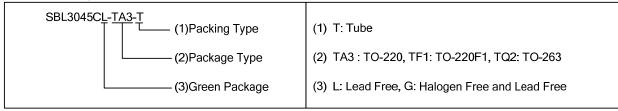
■ SYMBOL



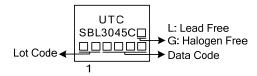
■ ORDERING INFORMATION

Ordering Number		Dealtone	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
SBL3045CL-TA3-T	SBL3045CG-TA3-T	TO-220	A1	K	A2	Tube	
SBL3045CL-TF1-T	SBL3045CG-TF1-T	TO-220F1	A1	K	A2	Tube	
SBL3045CL-TQ2-T	SBL3045CG-TQ2-T	TO-263	A1	K	A2	Tube	
SBL3045CL-TQ2-R	SBL3045CG-TQ2-R	TO-263	A1	K	A2	Tape Reel	

Note: Pin Assignment: A1: Anode K: Cathode A2: Anode



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Recurrent Peak Reverse Voltage		V_{RRM}	45	V	
RMS Voltage		V_{RWM}	45	V	
RMS Reverse Voltage		$V_{R(RMS)}$	32	V	
DC Blocking Voltage		V_R	45	V	
Average Forward Rectified Current	Per Leg	l _o	15	٨	
	Per Package		30	Α	
Peak Forward Surge Current 8.3ms Single Half			400	Α	
Sine-Wave Superimposed on Rated Load Per Diode		I _{FSM}	180		
Critical Rate of Rise of Reverse Voltage		dV/dt	10000	V/µs	
Operating Junction Temperature		T_J	150	°C	
Storage Temperature		T _{STG}	-65~+150	°C	

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2.
$$\frac{dPtot}{dT_J} < \frac{1}{Rth(j-a)}$$
 thermal runaway condition for a diode on its own heatsink

■ THERMAL RESISTANCES

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
Junction to Case	TO-220/TO-263	0	1.60	°C/W
	TO-220F1	θ_{JC}	3.31	°C/W

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 3)	$V_{(BR)R}$	I _R =0.50mA	45			V
For a DV-Horo Door	V_{FM}	T _J =25°C, I _F =15A		0.56	0.62	V
Forward Voltage Drop		T _J =125°C, I _F =15A			0.57	V
Landana O anna (Alada O)	I IDM	V _R =V _{RM} , T _J =25 °C			1	mA
Leakage Current (Note 3)		V _R =V _{RM} , T _J =125 °C			75	mA

Notes: 1. Pulse Test: 380µs pulse width, 2% duty cycle.

- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
- 3. Short duration pulse test used to minimize self-heating effect.
- 4. Thermal resistance junction to case mounted on heatsink.
- 5. Mounted on an FR4 PCB, single-sided copper, with 100 cm² copper pad area.

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