UNISONIC TECHNOLOGIES CO., LTD

UH8102 cmos ic

LOW POWER HALL EFFECT SWITCH

DESCRIPTION

UH8102 is a low-power integrated Hall switch designed to sense the applied magnetic flux density and give a digital output, which indicates the present condition of the magnitude sensed.

It mainly designed for battery-powered system and hand-held equipment, such as cellular flip-phones and PDA's, in which power consumption is one major concern. The typical power consumption of UH8100 at down to 10uW in 2.7V supply.

For **UH8102A**, the output will be at the "Low" level when no magnetic field is applied. When the applied magnetic flux density is stronger than the switching threshold, the output would be at the "High" level.

For **UH8102B**, the output will be at the "High" level when no magnetic field is applied. When the applied magnetic flux density is stronger than the switching threshold, the output would be at the "Low" level.

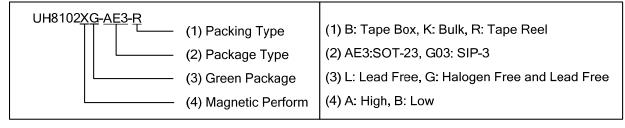


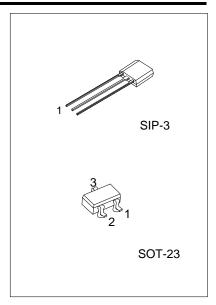
- *Micropower Operation
- *2.4V to 5.5V Battery Operation
- *Offset Canceling Technology
- *Superior Temperature Stability
- *Extremely Low Switch-Point Drift
- *Insensitive to Physical Stress

■ ORDERING INFORMATION

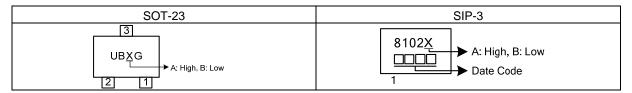
Ordering Number	Dookogo	Pin Assignment		Packing	
	Package 1 2		3		
UH8102XG-AE3-R	SOT-23	0	I G		Tape Reel
UH8102XG-G03-B	SIP-3	_	G	0	Tape Box
UH8102XG-G03-K	SIP-3	I G O		Bulk	

Note: Pin Assignment: O: Output I: V_{DD} G: GND

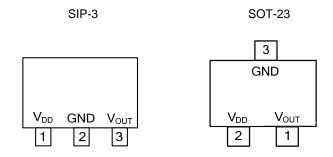




■ MARKING



■ PIN CONFIGURATION

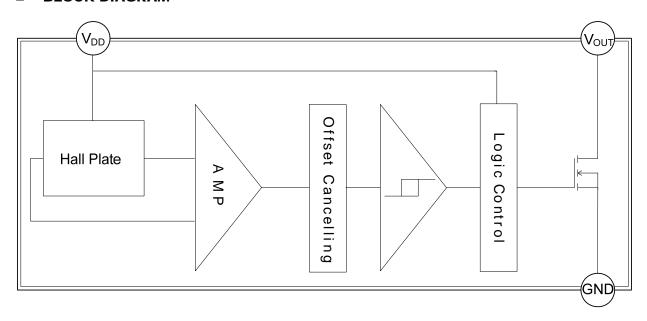


■ PIN DESCRIPTION

PIN NAME	PIN TYPE	PIN DESCRIPTION
V_{OUT}	0	Digital Output
V_{DD}	Р	Power Supply
GND	G	Ground

Note: O=Output, P=Power Supply, G=Ground

■ BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATING

PARAMETER		SYMBOL	RATINGS	UNIT
Magnetic Flux Density		В	Unlimited	mT
Supply Voltage		V_{DD}	5.5	V
Supply current		ΙQ	-1 ~ + 2.5	mA
D D: : ::	SIP-3	Ь	400	mW
Power Dissipation	SOT-23	P_{D}	200	mW
Junction Temperature		T_J	150	°C
Operation Temperature		T _{OPR}	-40 ~ + 85	°C
Storage Temperature		T _{STG}	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS (T_A=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{DD}	Operating	2.4	2.7	5.5	V
Output Voltage	V _{OUT}		-0.3	2.7	5.5	V
Ambient Temperature	T _A		-40	25	85	°C

■ ELECTRICAL CHARACTERISTICS (T_A=25°C)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Output Saturation Voltage	V_{SAT}	V _{DD} =2.7V			0.1		V
Output Leakage Current	l _{OFF}	V DD=2.7 V			0.01		uA
Supply Current	I _{DD(EN)}		Chip enable		1.1		mΑ
	I _{DD(DIS)}		Chip disable		2.5		uA
	I _{DD(AVG)}		Average supply current		3	20	uA
Operating Time	T_OP				60		us
Standby Time	T_{SD}	$V_{DD}=2.7V$			150		ms
Duty Cycle	D.C.				0.04		%

■ MAGNETIC CHARACTERISTICS (T_A=25°C,V_{DD}=2.7V)

For UH8102A (**High** level when no magnetic field is applied)

Rank	PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
1	Operation Points	B _{OP}	South or North pole to branded side, $ B > B_{OP} $, V_{OUT} On	8		23	
	Release Points	B _{RP}	South or North pole to branded side, $ B < B_{RP} $, V_{OUT} Off	10		25	Gauss
	Hysteresis	B _{OP} -B _{RP}	B _{OPX} -B _{RPX}		10		
	Operation Points	B _{OP}	South or North pole to branded side, $ B > B_{OP} $, V_{OUT} On	15		35	
2	Release Points	B _{RP}	South or North pole to branded side, B < B _{RP} , V _{OUT} Off	20		40	Gauss
	Hysteresis	$ B_{OP}-B_{RP} $	B _{OPX} -B _{RPX}		10		
3	Operation Points	B _{OP}	South or North pole to branded side, $ B > B_{OP} $, V_{OUT} On	1			
	Release Points	B _{RP}	South or North pole to branded side, $ B < B_{RP} $, V_{OUT} Off			70	Gauss
	Hysteresis	B _{OP} -B _{RP}	B _{OPX} -B _{RPX}		10		

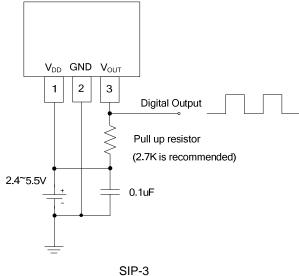
■ MAGNETIC CHARACTERISTICS(Cont.)

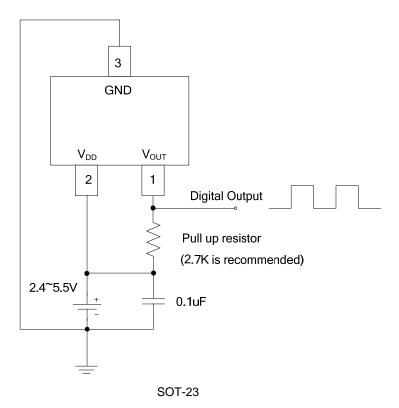
For UH8102B (Low level when no magnetic field is applied)

Rank	PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
1	Operation Points	B _{OP}	South or North pole to branded side, $ B > B_{OP} $, V_{OUT} On	10		25		
	Release Points	B _{RP}	South or North pole to branded side, B < B _{RP} , V _{OUT} Off	8		23	Gauss	
	Hysteresis	B _{OP} -B _{RP}	B _{OPX} -B _{RPX}		10			
2	Operation Points	B _{OP}	South or North pole to branded side, B > Bop , Vout On	20		40		
	Release Points	B _{RP}	South or North pole to branded side, B < B _{RP} , V _{OUT} Off	15		35	Gauss	
	Hysteresis	B _{OP} -B _{RP}	B _{OPX} -B _{RPX}		10			
3	Operation Points	B _{OP}	South or North pole to branded side, $ B > B_{OP} $, V_{OUT} On			70		
	Release Points	B _{RP}	South or North pole to branded side, $ B < B_{RP} $, V_{OUT} Off	1			Gauss	
	Hysteresis	B _{OP} -B _{RP}	B _{OPX} -B _{RPX}		10			

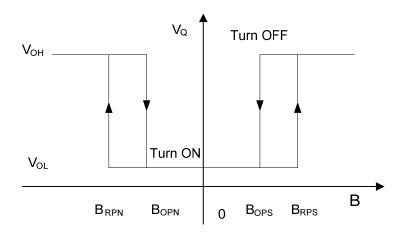
UH8102 CMOS IC

TYPICAL APPLICATION CIRCUIT

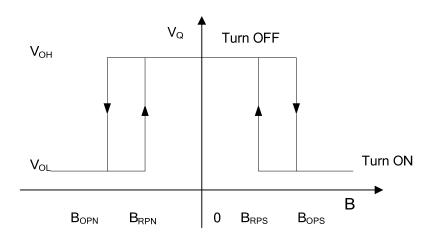




■ MAGNETIC FLUX



UH8102A (High level when no magnetic field is applied)



UH8102B (Low level when no magnetic field is applied)

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