# UNISONIC TECHNOLOGIES CO., LTD

P1580

**Preliminary** 

## LINEAR INTEGRATED CIRCUIT

# 380KHz, 2.5A STEP-DOWN SWITCHING REGULATOR

#### **■** DESCRIPTION

The UTC **P1580** is a current mode, PWM controller with 380kHz fixed frequency. It achieves 2.5A continuous output current over a wide input supply range with excellent load and line regulation. By using an external compensation pin, this device offers user flexibility in determining loop dynamic.

The UTC **P1580** integrates control, monitor and protection functions to provide a low cost and perfect power solution. The device provides 3.5 to 28V wide range operating input and high-efficiency up to 90%.

An Under- Voltage-Lock-Output (UVLO) circuit monitors the supply voltage to prevent from wrong logic control. An internal 1.222V reference voltage provides low output voltage down to 1.22V for further applications. The over-current protection of controller monitors the output current by using the voltage drop across a current sensing resistor. Additional under voltage protection monitors the voltage on FB pin for short-circuits protection.

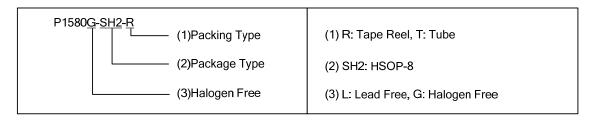
The UTC **P1580** provides fast transient response and requires very few external devices for operation.



- \* 2.5A Output Current
- \* 380kHz Frequency of Operation
- \* 3.5V to 28V Input Voltage Range
- \* 5µA Shutdown Supply Current
- \* Output Adjustable From 1.22 to 21
- \* Frequency Feedback at Short Circuit
- \* Thermal Shutdown
- \* Under Voltage Lock Output
- \* Current Mode With Low ESR Output Ceramic Capacitors
- \* Up to 90% Efficiency
- \* Frequency Synchronization Input

## ■ ORDERING INFORMATION

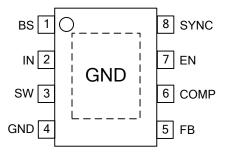
Ordering Number		Doolsono	Dealing	
Lead Free	Halogen Free	Package	Packing	
P1580L-SH2-R	P1580G-SH2-R	HSOP-8	Tape Reel	
P1580L-SH2-T	P1580G-SH2-T	HSOP-8	Tube	





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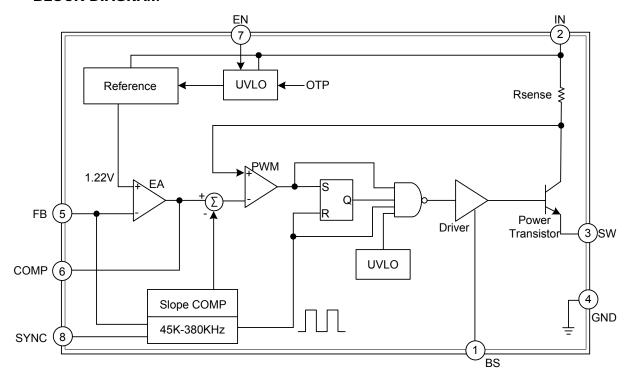
#### **■ PIN CONFIGURATION**



#### **■ PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION
1	BS	Supply pin to the power transistor driver. Tie to external circuit to generate a local supply voltage higher than the input voltage in order to fully turn on the internal power transistor.
2	IN	Power Supply pin.
3	SW	Power Switch Output pin.
4	GND	Ground pin.
5	FB	The output voltage feedback pin. It is also the inverting input of the error amplifier.
6	COMP	Compensation pin. It is also the output of the internal error amplifier.  (1). A RC network at this pin compensates the control loop.  (2). The voltage at this pin controls the peak current of the internal switch.
7	EN	Regulator On/Off Control pin. Leave EN unconnected if unused. A low input at EN turns on the converter, and a high input turns it off.
8	SYNC	Synchronization Input - The sync pin is used to synchronize the internal oscillator to an external signal.

#### **■ BLOCK DIAGRAM**



#### **ABSOLUTE MAXIMUM RATING (Note 3)**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{IN}$	28	V
Switch Voltage	$V_{SW}$	-1~ V <sub>IN</sub> +1	V
Boost Voltage	$V_{BS}$	V <sub>SW</sub> +6	V
Feedback Voltage	$V_{FB}$	-0.3~6	V
Enable/UVLO Voltage	V <sub>EN</sub>	-0.3~6	V
Compensation Voltage	V <sub>COMP</sub>	-0.3~6	V
Synchronization Voltage	V <sub>SYNC</sub>	-0.3~6	V
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>STG</sub>	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **RECOMMENDED OPERATING CONDITIONS** (Note 1)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V <sub>IN</sub>	3.5~28	٧
Ambient Operating Temperature	T <sub>A</sub>	-40 ~ +125	°C

Note: The device is not guaranteed to function outside its operating rating.

#### THERMAL DATA (Note 1)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	105	°C/W
Junction to Case	$\theta_{JC}$	50	°C/W

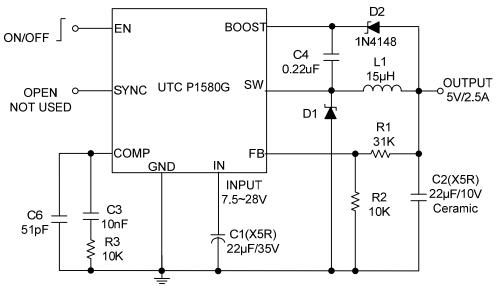
Note: Measured on approximately 1" square of 1 oz. Copper surrounding device leads.

#### ELECTRICAL CHARACTERISTICS (V<sub>IN</sub>=12V, T<sub>A</sub>=25°C, unless otherwise specified)

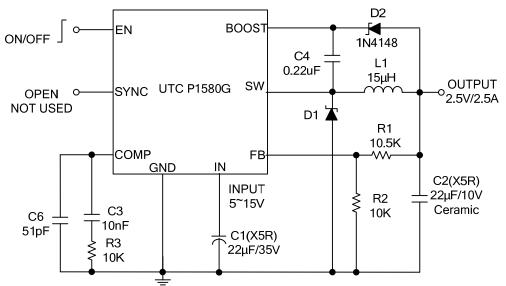
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Feedback Voltage		$V_{FB}$	$7V \le V_{IN} \le 25V$ , $V_{COMP} < 2V$	1.198	1.222	1.246	V
Upper Switch Leakage			V <sub>EN</sub> =0V, V <sub>SW</sub> =0V		0.1	10	μΑ
Current Limit		I <sub>LIMIT</sub>		3.1	3.3	3.3	Α
Current Limit Gain.					2.2		A/V
Output Current to Comp Pin Voltage		G <sub>COMP</sub>			2.2		AVV
Error Amplifier Voltage Gain		$A_{VEA}$			400		V/V
Error Amplifier Transconductance		$G_{EA}$	ΔI <sub>C</sub> =±10μA	680	770	1000	μΑ/V
Oscillator Frequency		Fosc		342	380	418	$KH_Z$
Short Circuit Frequency		$F_{SC}$	V <sub>FB</sub> =0V	40	46	54	$KH_Z$
Synchronization Frequency	1	Fsync	Sync Drive 0 ~ 2.7V	370		660	$KH_Z$
Duty Cycle	Maximum	$D_{MAX}$	V <sub>FB</sub> =1.0V		90		%
Duty Cycle	Minimum	$D_{MIN}$	V <sub>FB</sub> =1.5V			0	%
Switch V <sub>CESAT</sub>			I <sub>SW</sub> =2A		400		mV
Enable Threshold		$V_{EN}$	I <sub>CC</sub> > 100μA	0.9	1.1	1.3	V
Enable Pull Up Current		$I_{EN}$	V <sub>EN</sub> =0V		0.1		μΑ
Supply Current	Shutdown	I <sub>SHDN</sub>	V <sub>EN</sub> ≤0.4V		5	8	μΑ
	Quiescent	ΙQ	V <sub>EN</sub> ≥2.6V; V <sub>FB</sub> =1.4V		1.7		mA
Current of Short Circuit		I <sub>SC</sub>	V <sub>IN</sub> =10V		1.8		Α
Thermal Shutdown		T <sub>OTP</sub>			160		°C

Note: If the  $V_{\text{IN}}$  voltage exceeding 23V under short circuit condition, there will be some risk.

#### **■ TYPICAL APPLICATION CIRCUIT**

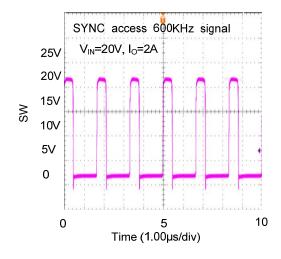


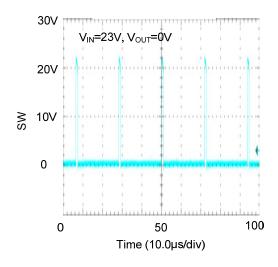
UTC **P1580** with 5V Output Voltage and  $22\mu F$  / 10V Ceramic Output Capacitor If the load current is applied in 2A, the input voltage can range from 7 to 28V.

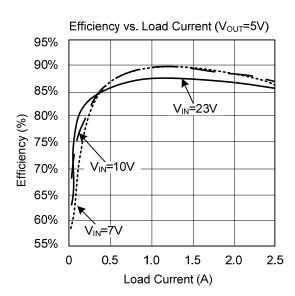


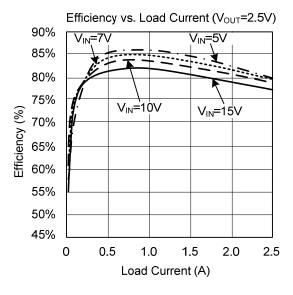
UTC P1580 with 2.5V Output Voltage and  $22\mu F$  / 10V Ceramic Output Capacitor If the load current is applied in 2A, the input voltage can range from 4.4 to 28V.

#### ■ TYPICAL CHARACTERISTICS









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